

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	3261	(714/700,7,814,815 713/503,401, 400 702/85,89).ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 12:02
L3	3884	(714/700,7,814,815 713/503,401, 400 702/85,89 370/508,517,371).ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 11:33
L4	103	L3 and deskew	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 11:33
L5	22	L4 and (FIFO "first-in first-out" "first-in-first-out" "first in-first out")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 11:42
L6	2	L5 and (BIST built-in self-test)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 11:44
L7	1	L5 and (BIST "built-in self-test")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 11:55
L8	1	L4 and (BIST "built-in self-test")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 11:55
L9	275	(714/700).ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 12:02
S1	250	Collins-H\$.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/12 11:11
S2	2	Collins-Hansel.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/13 12:24
S3	4	"942377".ap.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/12 11:16
S4	1	"6031847".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/13 12:24
S5	208	714/700.ccls.	USPAT	OR	OFF	2004/02/17 15:13
S6	35	714/700.ccls. and deskew	USPAT	OR	OFF	2004/02/17 15:06
S7	0	714/700.ccls. and deskew and link\$1reset	USPAT	OR	OFF	2004/02/17 15:07

S8	0	714/700.ccls. and deskew and link adj reset	USPAT	OR	OFF	2004/02/17 15:07
S9	1	deskew same dynamic near4 skew near4 compensation	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/17 15:16
S10	2	deskew and dynamic near4 skew near4 compensation	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/17 15:16
S11	190	fence near4 pattern	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/17 15:17
S12	0	(714/700.ccls. and deskew and synchronous) and (fence near4 pattern)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/17 15:17
S13	15	714/700.ccls. and deskew and synchronous	USPAT	OR	OFF	2004/02/17 15:24
S14	6	Chesson-G\$.in.	USPAT	OR	OFF	2004/02/17 15:25
S15	9	Chesson-G\$.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/17 15:25
S16	7	Chesson-Greg\$.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/17 15:29
S17	1141	(714/700,7,814,815,713/503,401, 400,702/85,89).ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/12/08 11:26
S18	44	deskew and ((714/700,7,814,815, 713/503,401,400,702/85,89).ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/18 11:01
S19	4	deskew same train\$4 and ((714/700,7,814,815,713/503,401, 400,702/85,89).ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/17 15:37
S20	23	skew near4 compensation and ((714/700,7,814,815,713/503,401, 400,702/85,89).ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/17 15:37
S21	8	(skew near4 compensation and ((714/700,7,814,815,713/503,401, 400,702/85,89).ccls.)) and link	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/17 15:50
S22	12	(skew near4 compensation and ((714/700,7,814,815,713/503,401, 400,702/85,89).ccls.)) and synchronous	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/17 15:54

S23	17	(skew near4 compensation and ((714/700,7,814,815,713/503,401, 400,702/85,89).ccls.)) and synchron\$5	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/18 10:19
S24	10	(skew near4 compensation and ((714/700,7,814,815,713/503,401, 400,702/85,89).ccls.)) and align\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/18 11:01
S25	1	"5978377".PN.	USPAT	OR	OFF	2004/02/18 10:25
S26	1	"6204948".PN.	USPAT	OR	OFF	2004/02/18 10:25
S27	1	((skew near4 compensation and ((714/700,7,814,815,713/503,401, 400,702/85,89).ccls.)) and align\$4) and signature	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/18 10:40
S28	1	((skew near4 compensat\$3 and ((714/700,7,814,815,713/503,401, 400,702/85,89).ccls.)) and align\$4) and signature	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/18 10:42
S29	23	(skew near4 compensat\$3 and ((714/700,7,814,815,713/503,401, 400,702/85,89).ccls.)) and align\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/18 10:55
S30	0	skew near4 compensat\$3 and align\$4 and "5157530".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/18 10:56
S31	1	("5157530" "5408473" "5793770").pn. and skew near4 compensat\$3 and align\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/18 11:00
S32	50	("4797951" "6522269" "6625675" "4433425" "5022056" "4926447" "4855735" "5425020" "5550875" "5644604" "5652767" "5712585" "5964845" "6038048" "6151428" "6211714" "6211714" "4630268" "4943984" "5020081" "5239562" "5267240" "5349654" "5422880" "5455830" "5455831" "5463488" "5513377" "5521736" "5568526" "5570356" "5625480" "5634004" "5694612" "5719862" "5768529" "5832047" "5925097" "6021457" "6026051" "6047021" "6085285" "6104209" "6128319" "6185693" "6192482" "6195764" "6374360" "6430696" "6434141").pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/18 11:00

S33	7	(("4797951" "6522269" "6625675" "4433425" "5022056" "4926447" "4855735" "5425020" "5550875" "5644604" "5652767" "5712585" "5964845" "6038048" "6151428" "6211714" "6211714" "4630268" "4943984" "5020081" "5239562" "5267240" "5349654" "5422880" "5455830" "5455831" "5463488" "5513377" "5521736" "5568526" "5570356" "5625480" "5634004" "5694612" "5719862" "5768529" "5832047" "5925097" "6021457" "6026051" "6047021" "6085285" "6104209" "6128319" "6185693" "6192482" "6195764" "6374360" "6430696" "6434141").pn.) and ((714/700,7,814,815,713/503,401, 400,702/85,89).ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/18 11:01
S34	822	((("4797951" "6522269" "6625675" "4433425" "5022056" "4926447" "4855735" "5425020" "5550875" "5644604" "5652767" "5712585" "5964845" "6038048" "6151428" "6211714" "6211714" "4630268" "4943984" "5020081" "5239562" "5267240" "5349654" "5422880" "5455830" "5455831" "5463488" "5513377" "5521736" "5568526" "5570356" "5625480" "5634004" "5694612" "5719862" "5768529" "5832047" "5925097" "6021457" "6026051" "6047021" "6085285" "6104209" "6128319" "6185693" "6192482" "6195764" "6374360" "6430696" "6434141").pn.) and ((714/700,7,814,815,713/503,401, 400,702/85,89).ccls.)) skew near4 compensat\$3 and align\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/18 11:02
S35	1	((("4797951" "6522269" "6625675" "4433425" "5022056" "4926447" "4855735" "5425020" "5550875" "5644604" "5652767" "5712585" "5964845" "6038048" "6151428" "6211714" "6211714" "4630268" "4943984" "5020081" "5239562" "5267240" "5349654" "5422880" "5455830" "5455831" "5463488" "5513377" "5521736" "5568526" "5570356" "5625480" "5634004" "5694612" "5719862" "5768529" "5832047" "5925097" "6021457" "6026051" "6047021" "6085285" "6104209" "6128319" "6185693" "6192482" "6195764" "6374360" "6430696" "6434141").pn.) and ((714/700,7,814,815,713/503,401, 400,702/85,89).ccls.)) and skew near4 compensat\$3 and align\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/02/18 11:03

S36	7	(US-6031847-\$ or US-6557110-\$ or US-6678842-\$ or US-6636993-\$ or US-5513377-\$).did. or (US-20030046618-\$ or US-20030208717-\$).did.	US-PGPUB; USPAT	OR	OFF	2004/02/18 11:28
S37	5	((US-6031847-\$ or US-6557110-\$ or US-6678842-\$ or US-6636993-\$ or US-5513377-\$).did. or (US-20030046618-\$ or US-20030208717-\$).did.) and align\$4	USPAT	OR	OFF	2004/02/18 11:47
S38	0	((US-6031847-\$ or US-6557110-\$ or US-6678842-\$ or US-6636993-\$ or US-5513377-\$).did. or (US-20030046618-\$ or US-20030208717-\$).did.) and signature	USPAT	OR	OFF	2004/02/18 11:24
S39	0	((US-6031847-\$ or US-6557110-\$ or US-6678842-\$ or US-6636993-\$ or US-5513377-\$).did. or (US-20030046618-\$ or US-20030208717-\$).did.) and calibration	USPAT	OR	OFF	2004/02/18 11:25
S40	0	((US-6031847-\$ or US-6557110-\$ or US-6678842-\$ or US-6636993-\$ or US-5513377-\$).did. or (US-20030046618-\$ or US-20030208717-\$).did.) and calibrat\$3	USPAT	OR	OFF	2004/02/18 11:26
S41	0	((US-6031847-\$ or US-6557110-\$ or US-6678842-\$ or US-6636993-\$ or US-5513377-\$).did. or (US-20030046618-\$ or US-20030208717-\$).did.) and detect\$3 near signature	USPAT	OR	OFF	2004/02/18 11:26
S42	1	((US-6031847-\$ or US-6557110-\$ or US-6678842-\$ or US-6636993-\$ or US-5513377-\$).did. or (US-20030046618-\$ or US-20030208717-\$).did.) and signature	US-PGPUB; USPAT	OR	OFF	2004/02/18 11:28
S43	7	((US-6031847-\$ or US-6557110-\$ or US-6678842-\$ or US-6636993-\$ or US-5513377-\$).did. or (US-20030046618-\$ or US-20030208717-\$).did.) and detect\$3	US-PGPUB; USPAT	OR	OFF	2004/02/18 11:29
S44	801963	((US-6031847-\$ or US-6557110-\$ or US-6678842-\$ or US-6636993-\$ or US-5513377-\$).did. or (US-20030046618-\$ or US-20030208717-\$).did.) and phase align\$4	USPAT	OR	OFF	2004/02/18 11:47

S45	1	((US-6031847-\$ or US-6557110-\$ or US-6678842-\$ or US-6636993-\$ or US-5513377-\$).did. or (US-20030046618-\$ or US-20030208717-\$).did.) and phase near align\$4	USPAT	OR	OFF	2004/02/18 11:47
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*Soo-Chang Pei; Ji-Hwei Horng;*  
 Pattern Recognition, 1996., Proceedings of the 13th International Conference on , Volume: 1 , 25-29 Aug. 1996  
 Pages:248 - 252 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(292 KB\)\]](#) IEEE CNF

**2 A low-jitter mixed-mode DLL for high-speed DRAM applications**

*Jae Joon Kim; Sang-Bo Lee; Tae-Sung Jung; Chang-Hyun Kim; Soo-In Cho; Beomsup Kim;*  
 Solid-State Circuits, IEEE Journal of , Volume: 35 , Issue: 10 , Oct. 2000  
 Pages:1430 - 1436

[\[Abstract\]](#) [\[PDF Full-Text \(380 KB\)\]](#) IEEE JNL

**3 Low-power clock-deskew buffer for high-speed digital circuits**

*Shen-Iuan Liu; Jiunn-Hwa Lee; Hen-Wai Tsao;*  
 Solid-State Circuits, IEEE Journal of , Volume: 34 , Issue: 4 , April 1999  
 Pages:554 - 558

[\[Abstract\]](#) [\[PDF Full-Text \(216 KB\)\]](#) IEEE JNL

**4 A design for digital, dynamic clock deskew**

*Dike, C.E.; Kurd, N.A.; Patra, P.; Barkatullah, J.;*  
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 Pages:21 - 24

[\[Abstract\]](#) [\[PDF Full-Text \(374 KB\)\]](#) IEEE CNF

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**5 An open-loop clock deskewing circuit for high-speed synchronous D***Changsik Yoo;*

Electronics Letters , Volume: 39 , Issue: 1 , 9 Jan. 2003

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[\[Abstract\]](#) [\[PDF Full-Text \(252 KB\)\]](#) [IEE JNL](#)

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**6 A 160-2550MHz CMOS active clock deskewing PLL using analog phase interpolation***Maxim, A.;*

Solid-State Circuits Conference, 2004. Digest of Technical Papers. ISSCC. 2004 IEEE International , 15-19 Feb. 2004

Pages:346 - 532 Vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(806 KB\)\]](#) [\[Extended Object\]](#) [IEEE CNF](#)

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**7 Receiver ASIC for timing, trigger and control distribution in LHC experiments***Christiansen, J.; Marchioro, A.; Moreira, P.; Sancho, A.;*

Nuclear Science Symposium and Medical Imaging Conference Record, 1995.,

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**8 A technique to deskew differential PCB traces***Atrash, A.H.; Butka, B.;*

Circuits and Systems, 2004. ISCAS '04. Proceedings of the 2004 International Symposium on , Volume: 2 , 23-26 May 2004

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**9 Time-deskew buffer design for MC-CDMA rake receiver***Lee, S.; Kim, J.; Eo, I.; Kim, K.;*

Electronics Letters , Volume: 37 , Issue: 14 , 5 July 2001

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**10 A 1 Gb/s deskew IC for RAMBUS automatic test application***Hendarman, A.; Choudhury, A.; Yousefi, A.; Armstrong, A.;*

Gallium Arsenide Integrated Circuit (GaAs IC) Symposium, 2000. 22nd Annual

8 Nov 2000

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**11 1.2 Gbps/pin simultaneous bidirectional transceiver logic with bit deskew technique***Fujimura, Y.; Takahashi, T.; Toyoshima, S.; Nagashima, K.; Baba, J.; Matsumoto, T.;*

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**12 Understanding smeared documents**

*Johnson, R.B.;*

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Pages:2/1 - 2/6

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**13 Comparison of the deskewed short-time correlator and the maximum likelihood correlator**

*Betz, J.;*

Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 32 , Issue: 2 , Apr 1984

Pages:285 - 294

[\[Abstract\]](#) [\[PDF Full-Text \(776 KB\)\]](#) [IEEE JNL](#)

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**14 Clock-deskew buffer using a SAR-controlled delay-locked loop**

*Guang-Kaai Dehng; June-Ming Hsu; Ching-Yuan Yang; Shen-Iuan Liu;*

Solid-State Circuits, IEEE Journal of , Volume: 35 , Issue: 8 , Aug. 2000

Pages:1128 - 1136

[\[Abstract\]](#) [\[PDF Full-Text \(264 KB\)\]](#) [IEEE JNL](#)

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**15 An 8Gb/s source-synchronous I/O link with adaptive receiver equalization, offset cancellation and clock deskew**

*Jaussi, J.E.; Balamurugan, G.; Johnson, D.R.; Casper, B.K.; Martin, A.; Kenney, J.T.; Shanbhag, N.; Mooney, R.;*

Solid-State Circuits Conference, 2004. Digest of Technical Papers. ISSCC. 2004 IEEE International , 15-19 Feb. 2004

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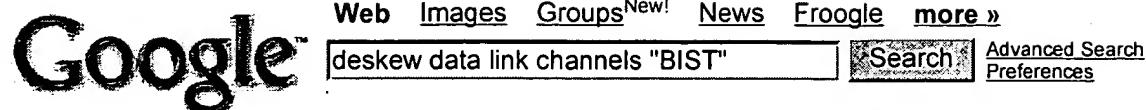
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... Internal **data** buffering ... MHz DDR - 1600 MT/s and Configurable bit/byte **deskew**) - 32-bit ... 8B/10B and 64/66 encoders/decoders • Programmable link aggregation for ...

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